

FIG. 4 PRIOR ART

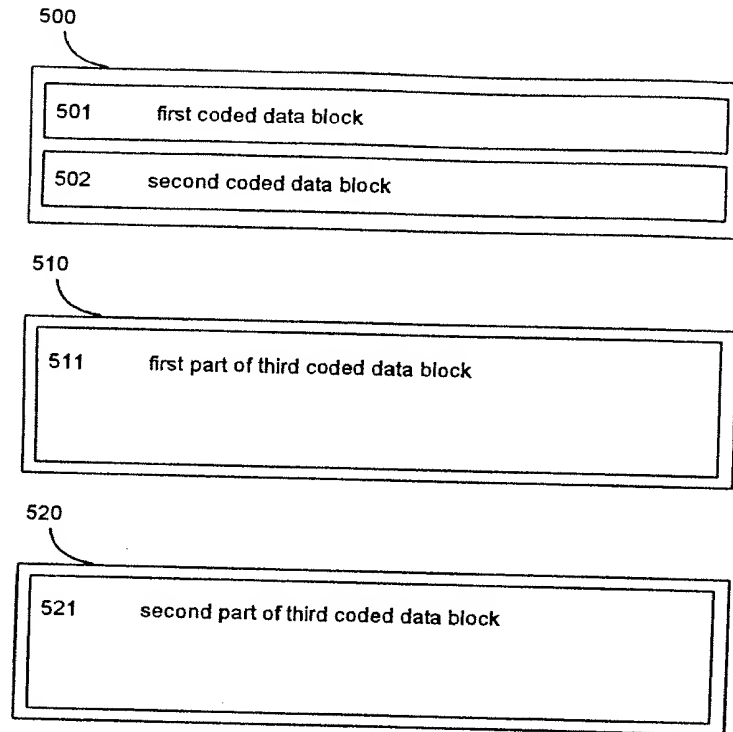


FIG. 5

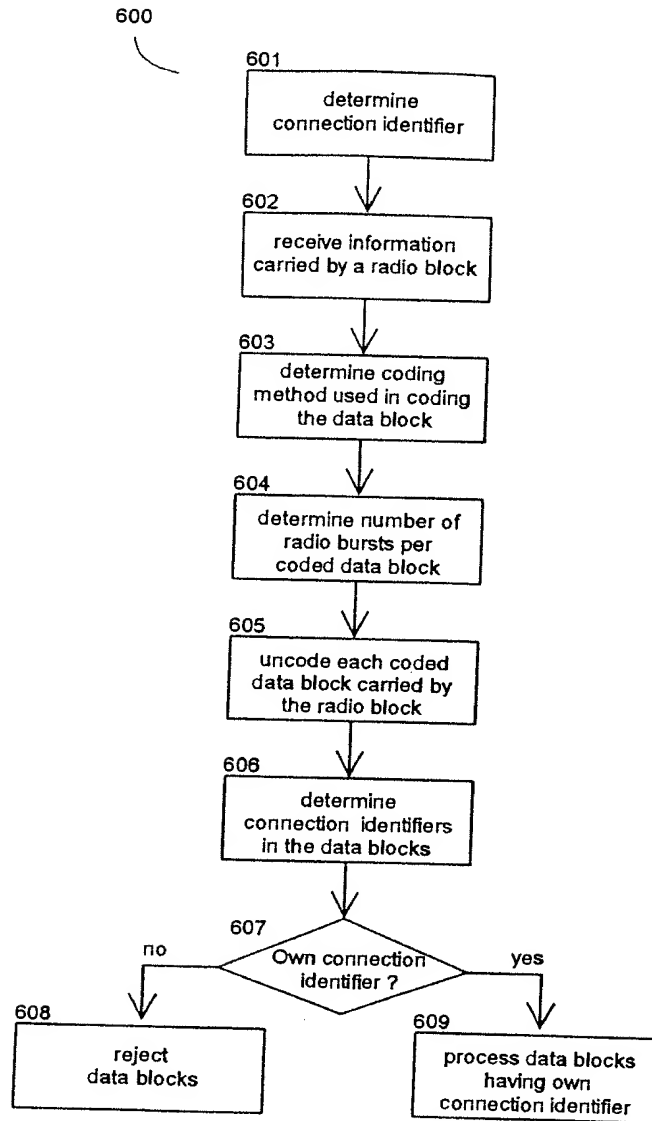


FIG. 6

FIG. 7a

700	
701 MAC header	702 USF
	703 RLC header (TFI1) + data 1/2
	704 RLC header (TFI1) + data 2/2
	705 RLC header (TFI2) + data 1/2
	706 RLC header (TFI2) + data 2/2

FIG. 7b

710	
711 MAC header	702 USF
	751 Bid1 = 1
	752 Bid2 = 2
	753 Bid3 = 2
	754 Bid4 = 1
	713 RLC header (TFI3) + data 1/1
	714 RLC header (TFI4) + data 1/2
	715 RLC header (TFI4) + data 2/2
	716 RLC header (TFI5) + data 1/1

FIG. 7c

720	
721 MAC header	702 USF = 5
	761 extUSF1 = 1
	762 extUSF2 = 1
	763 extUSF3 = 2
	764 extUSF4 = 3
	723 RLC header (TFI6) + data 1/2
	724 RLC header (TFI6) + data 2/2
	725 RLC header (TFI7) + data 1/2
	726 RLC header (TFI7) + data 2/2

FIG. 7d

730	
731 MAC header	702 USF = 5
	761 extUSF1 = 1
	762 extUSF2 = 1
	763 extUSF3 = 1
	764 extUSF4 = 3
	771 Blu1 = 1
	772 Blu2 = 2
	773 Blu3 = 2
	774 Blu4 = 1
	733 RLC header (TFI6) + data 1/2
	734 RLC header (TFI6) + data 2/2
	735 RLC header (TFI7) + data 1/2
	736 RLC header (TFI7) + data 2/2

FIG. 7e

740	
741 MAC header	702 USF = 5
	761 extUSF1 = 1
	762 extUSF2 = 1
	763 extUSF3 = 1
	764 extUSF4 = 3
	771 Blu1 = 1
	772 Blu2 = 2
	773 Blu3 = 2
	774 Blu4 = 1
	751 Bid1 = 1
	752 Bid2 = 2
	753 Bid3 = 2
	754 Bid4 = 1
	743 RLC ... (TFI3) + ... 1/1
	744 RLC ... (TFI4) + ... 1/2
	745 RLC ... (TFI4) + ... 2/2
	746 RLC ... (TFI5) + ... 1/1

800

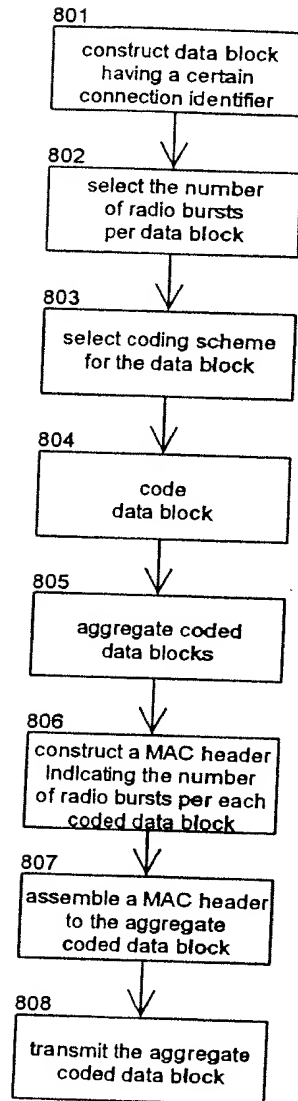


FIG. 8

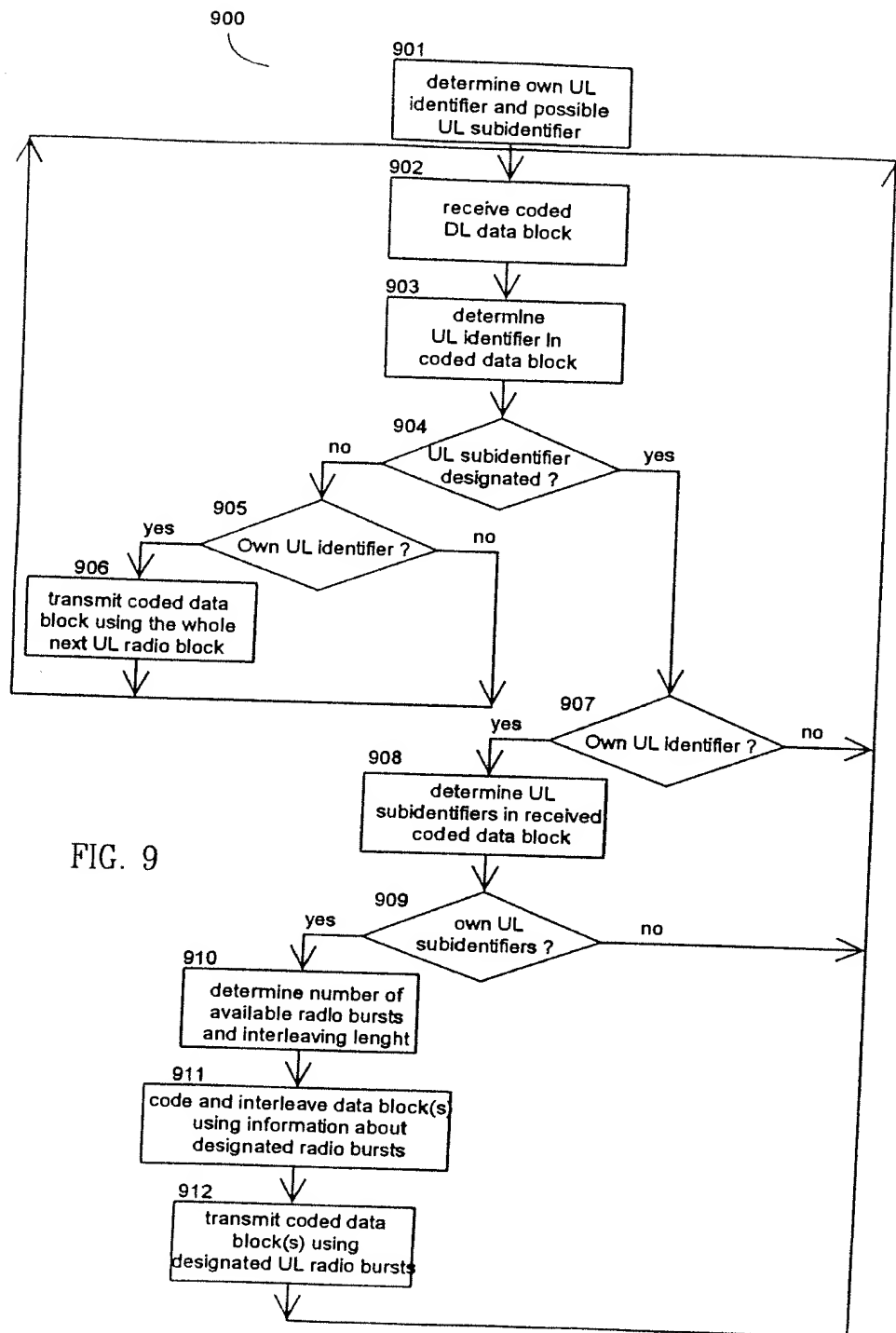


FIG. 9

1000

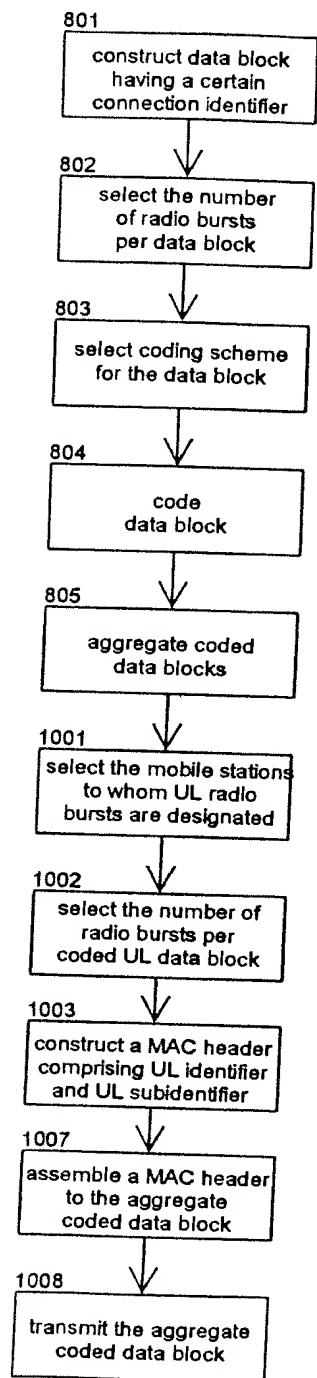


FIG. 10

